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| Not started | Paused | Working on | Almost finished | Done |

* Motivation.
  + Price comparison: Tek AWG + Alazar v.s. ZCU216.
* Firmware
  + Introduction
    - System overview
    - Fpga & computer architecture basics.
  + Vivado
  + Ip
  + bd
* Software.
  + Introduction
    - System overview
  + Pynq.
    - Introduction
    - Some important classes
      * MMIO
      * Overlay
      * DefaultIP
  + Qick’s python lib.
    - QickSoc
    - QickConfig
    - QickProgram
    - AveragerProgram
    - RAveragerProgram
* Hardware.
  + Components.
    - Xm566 daughter board.
    - 10MHz Rubidium clock.
    - Local oscillator - sgs100a.
    - Amplifier, mixer, splitter, DC-block, attenuator.
  + Issues.
    - Rf component’s non-linearity.
    - ZCU PLL settings & sync with 10MHz Rubidium clock.
* Experiment.
  + Signal integrity debugging
    - Sampling & re-construction.
  + Qubit measurement
    - Setup.
    - Comparison with r&s VNA on oneTone measurement.